

ABSTRACT OF THE DISCLOSURE

An array substrate device includes a gate line formed on a substrate extending along a first direction having a gate electrode, a data line formed on the substrate extending along a second direction having a data pad disposed apart from a first end of the data line, the data and gate lines defining a pixel region, a gate pad formed on the substrate disposed apart from a first end of the gate line, a thin film transistor formed at a crossing region of the gate and data lines and including the gate electrode, a semiconductor layer, a source electrode, and a drain electrode, a black matrix overlapping the thin film transistor, the gate line, and the data line except for a first portion of the drain electrode, a first pixel electrode at the pixel region contacting the first portion of the drain electrode and the substrate, a color filter on the first pixel electrode at the pixel region, and a second pixel electrode on the color filter contacting the first pixel electrode.